

FIG. 1 (PRIOR ART)

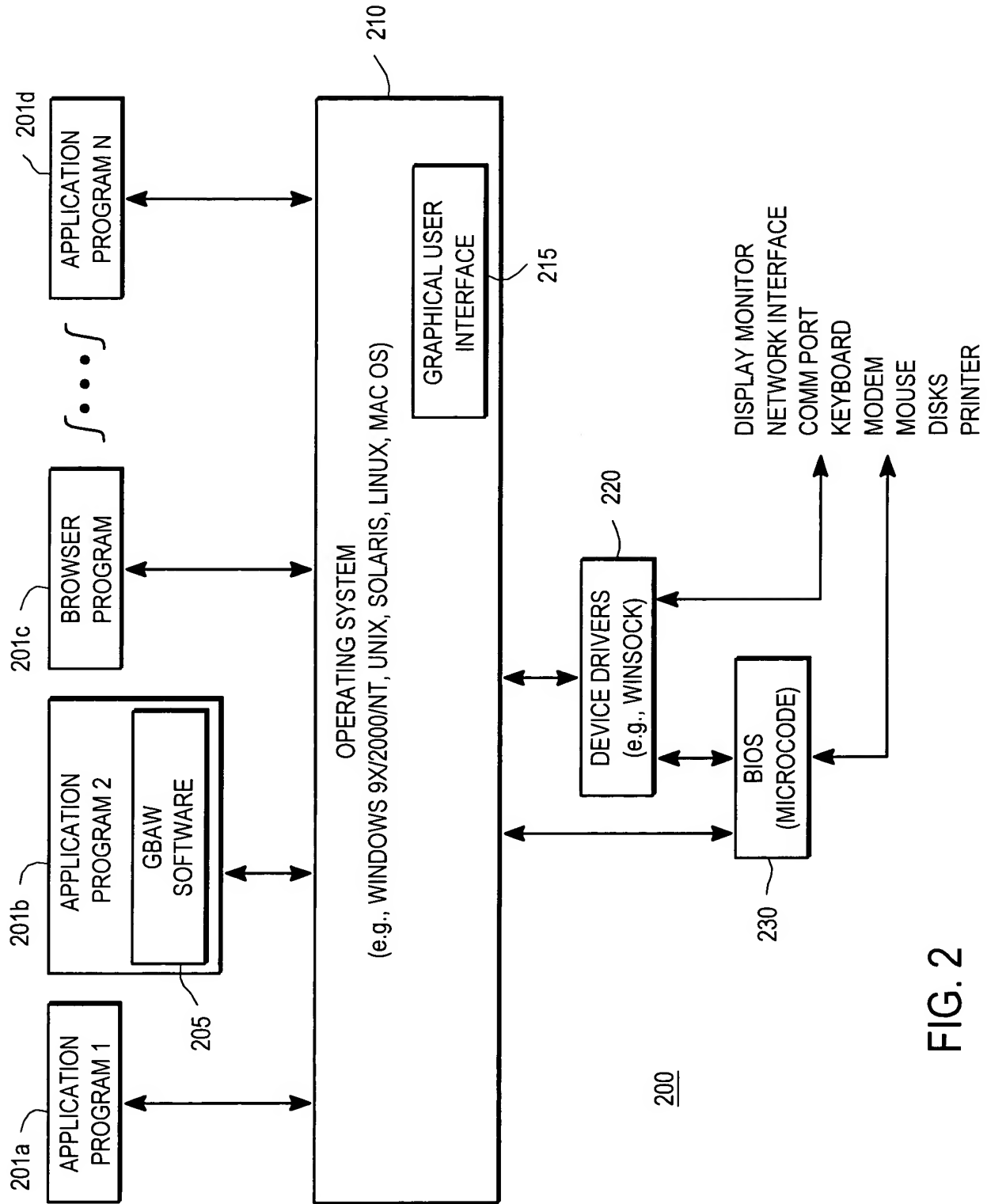


FIG. 2

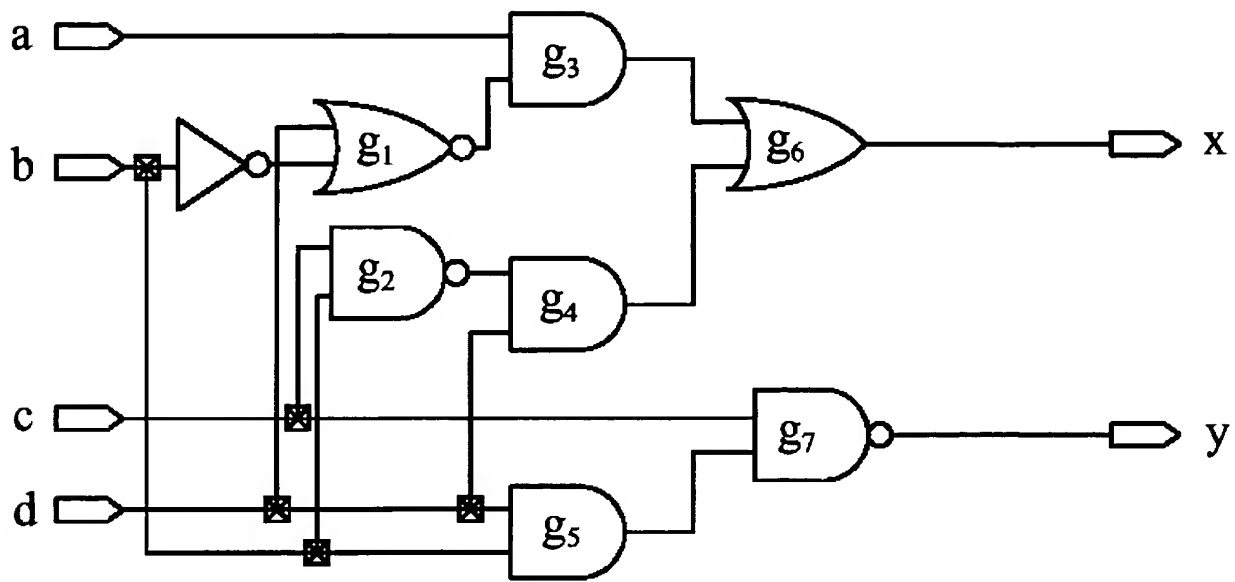


FIG. 3A

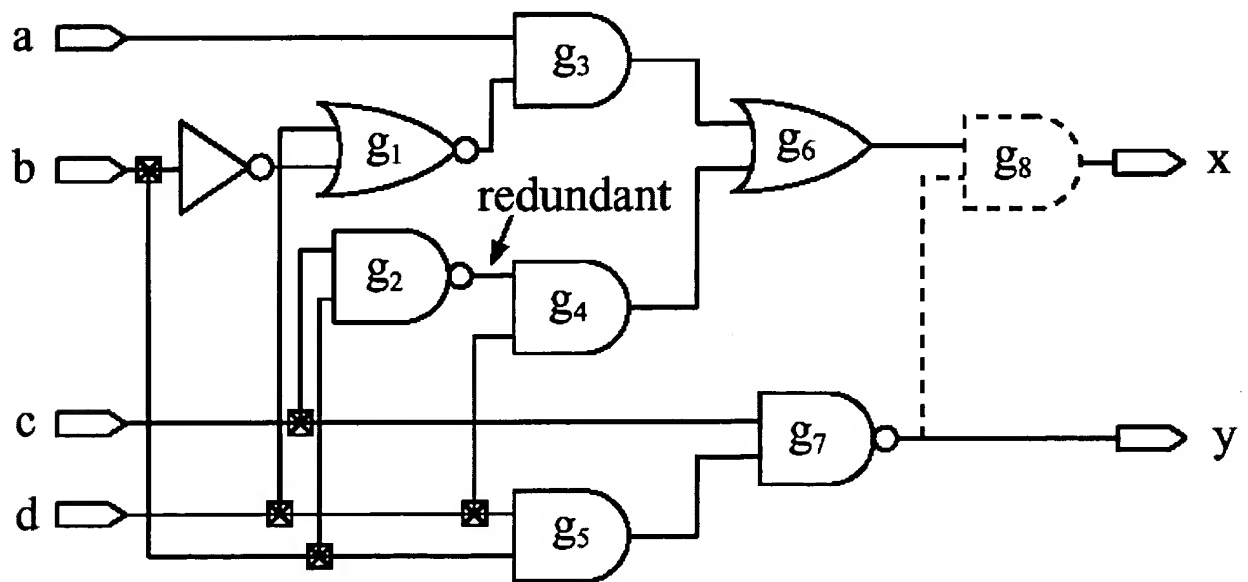


FIG. 3B

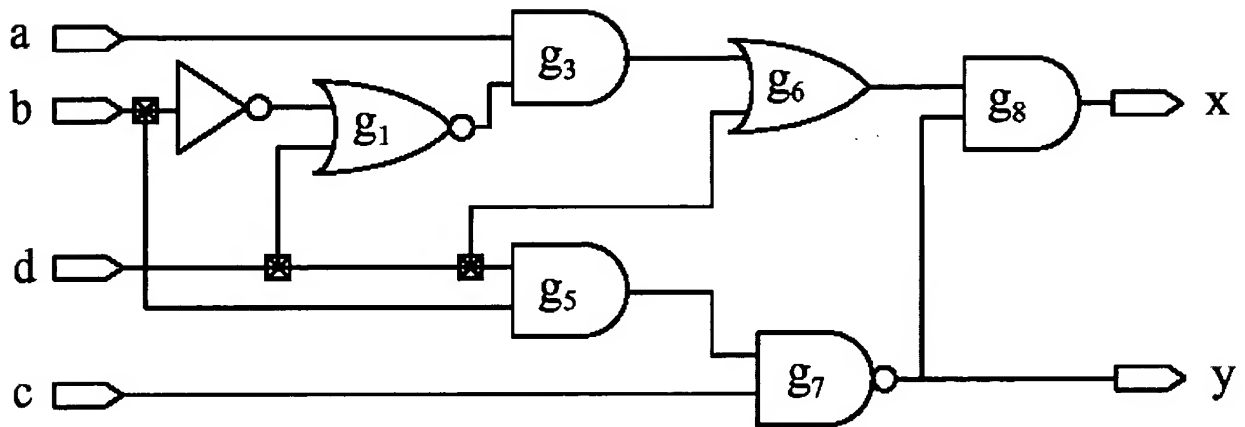


FIG. 3C

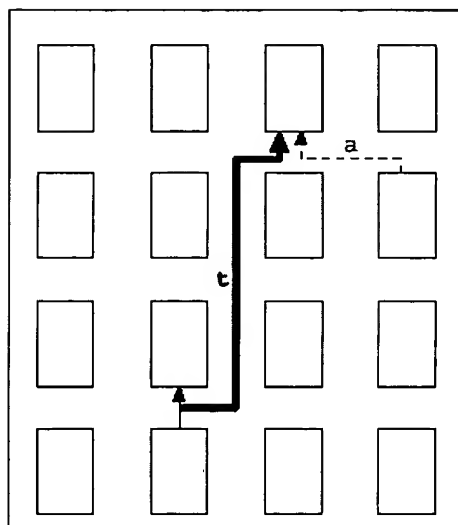


FIG. 3D

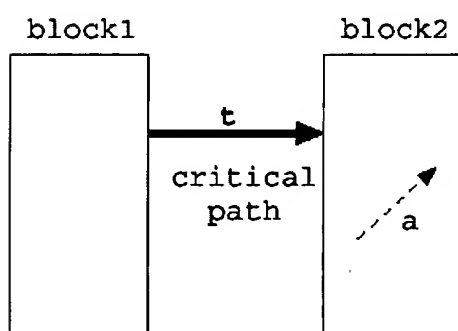


FIG. 3E

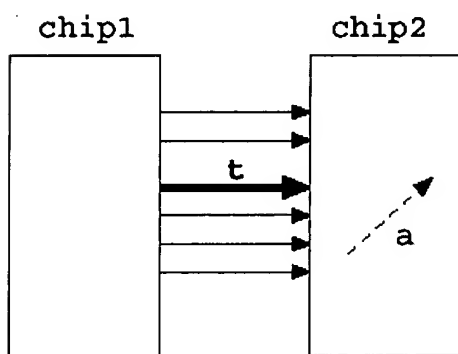


FIG. 3F

410

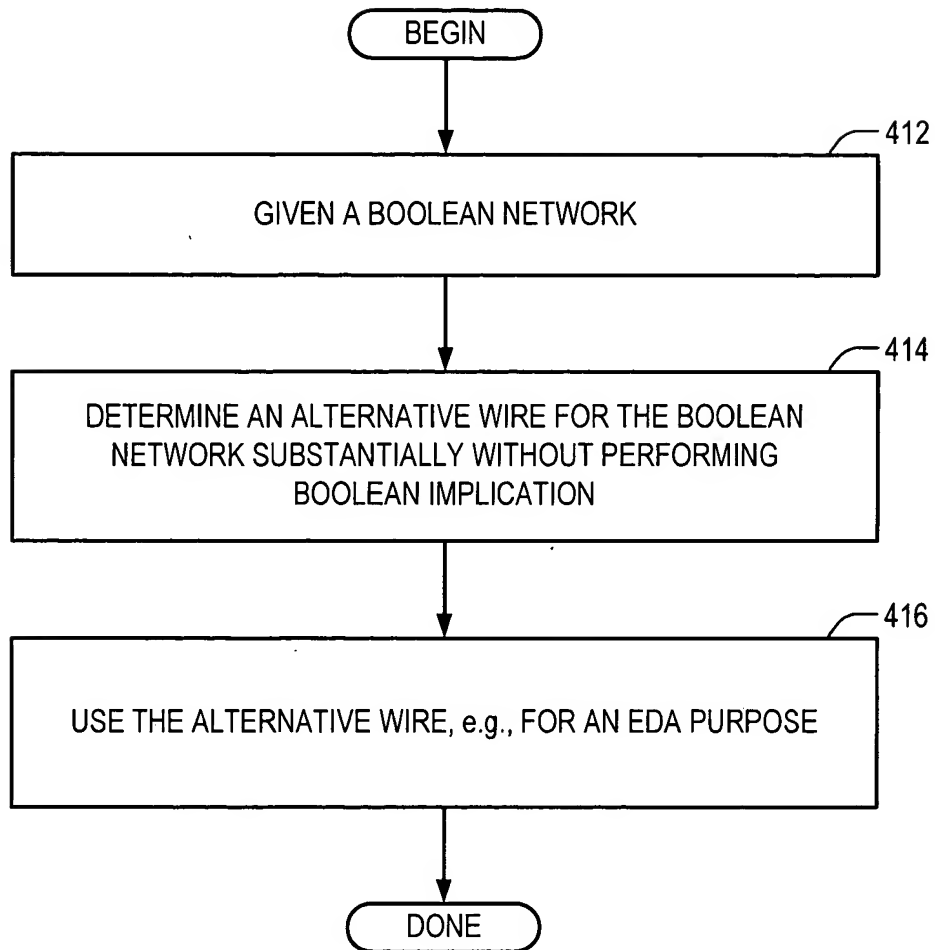


FIG. 4

510

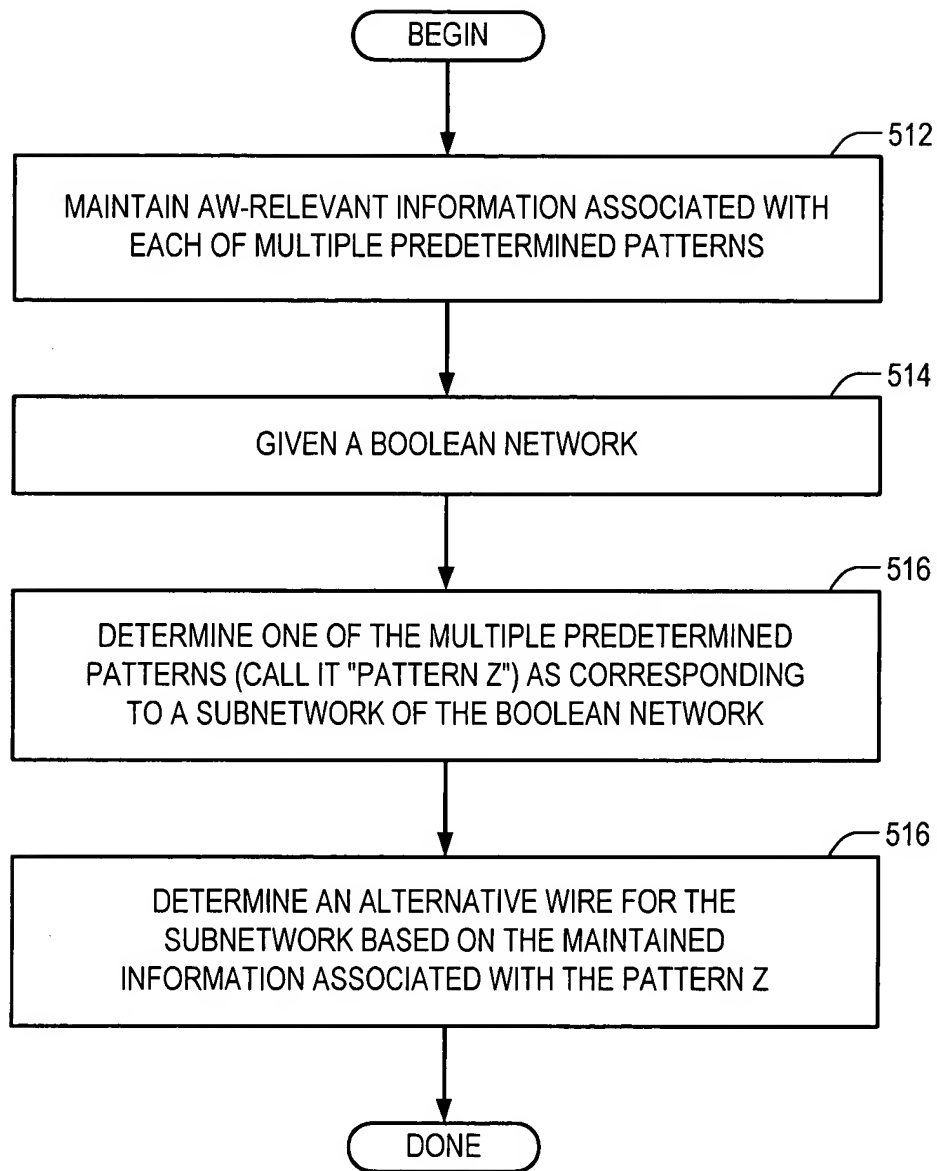


FIG. 5

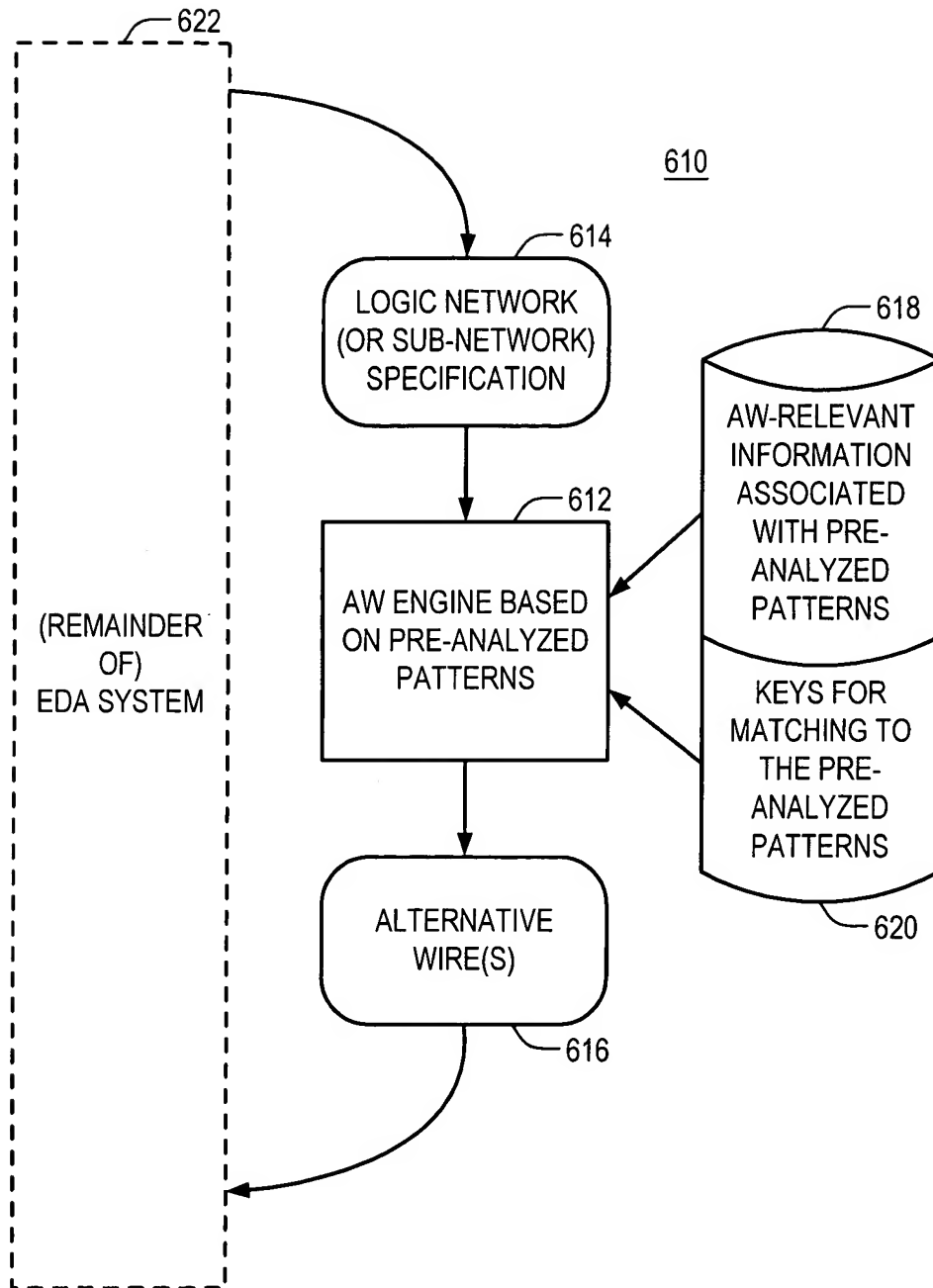


FIG. 6



710

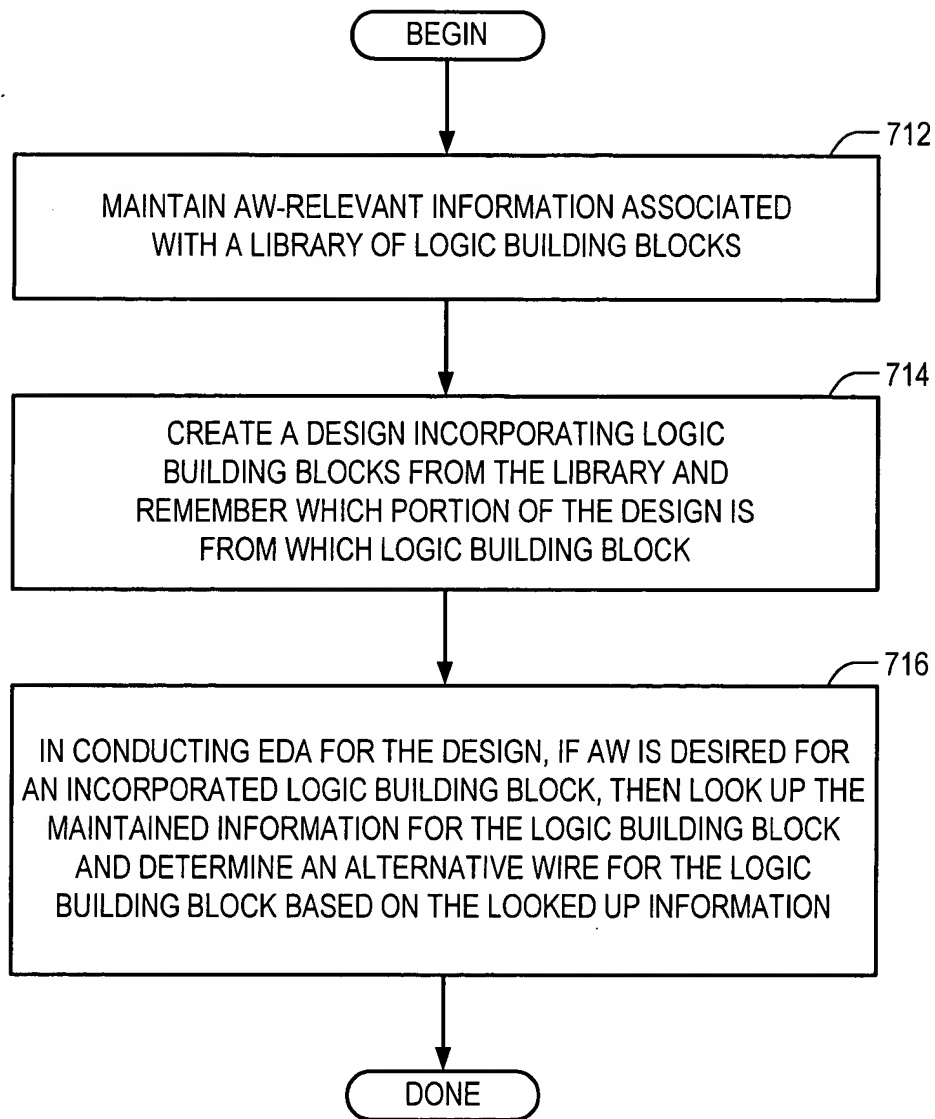


FIG. 7

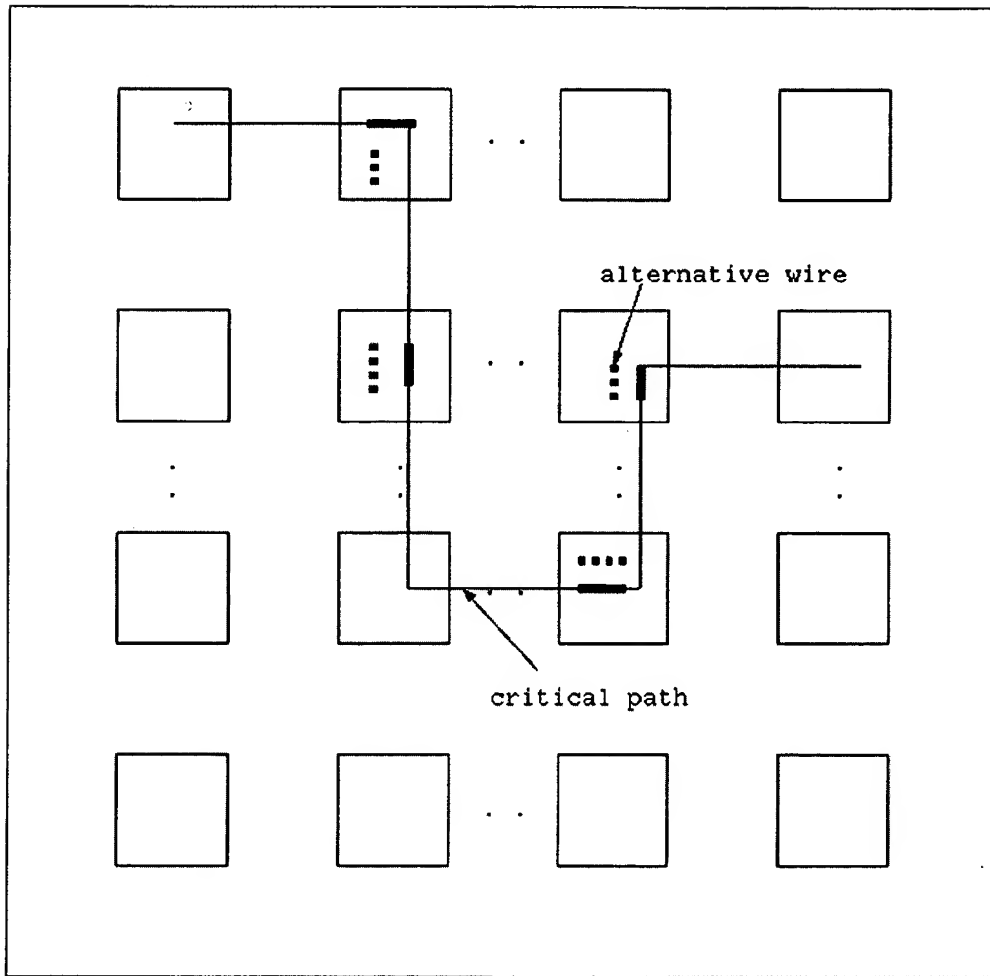


FIG. 8

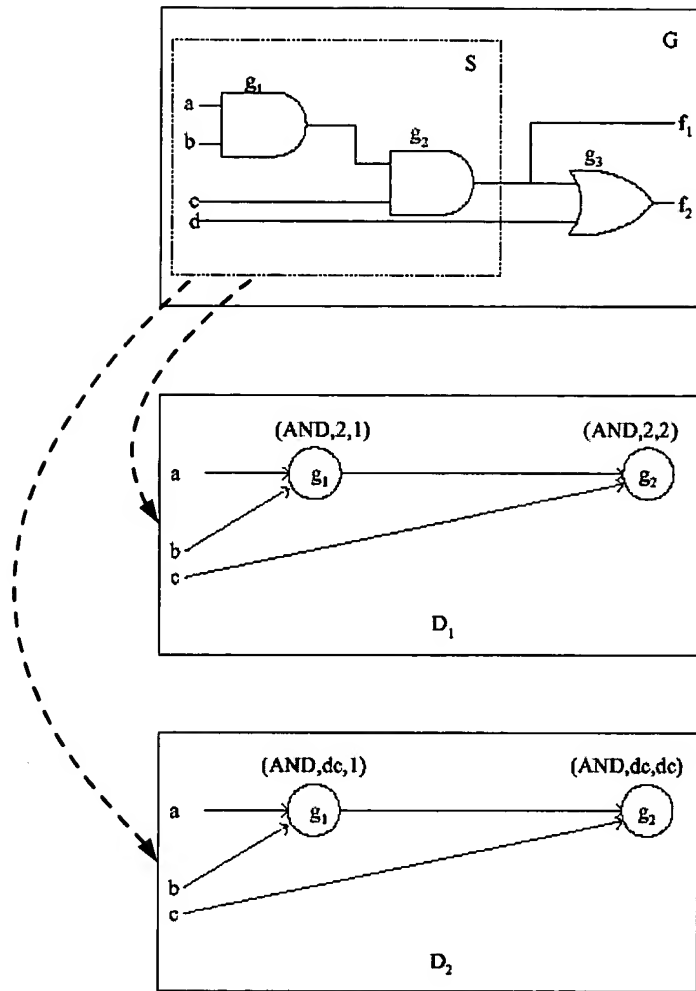


FIG. 9

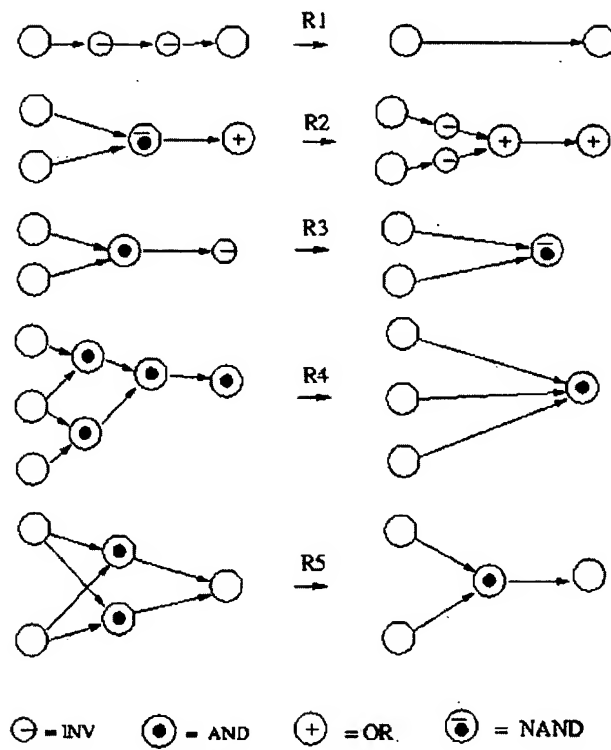


FIG. 10

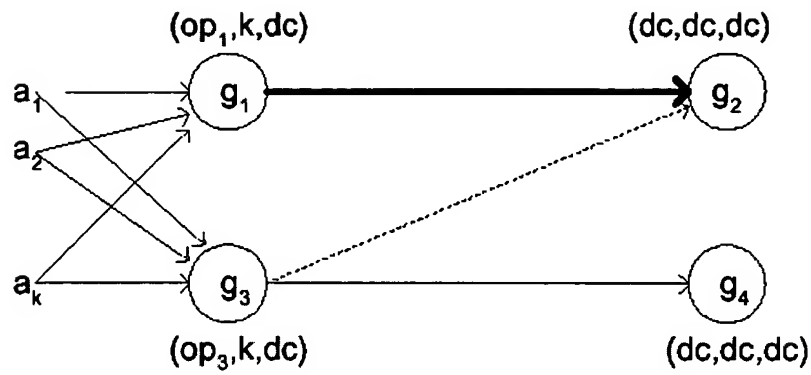
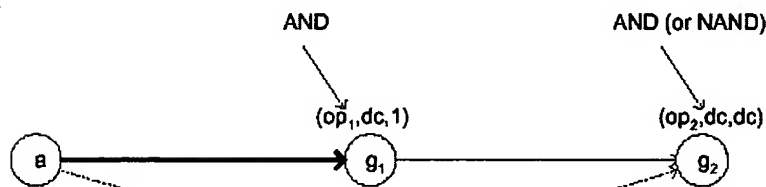
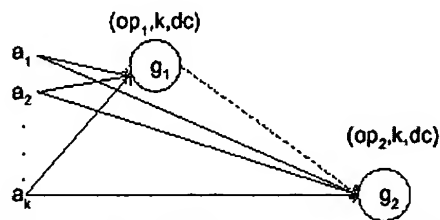


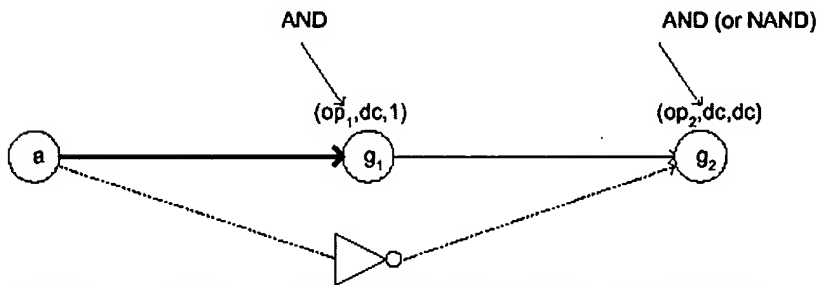
FIG. 11



(a) Case 1-1,  $op_1=AND$ ,  $op_2=AND$  (or NAND); or  $op_1=OR$ ,  $op_2=OR$ (or NOR)

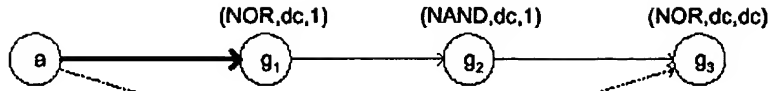


(b) Case 1-2,  $op_1=AND$ ,  $op_2=AND$  (or NAND); or  $op_1=OR$ ,  $op_2=OR$ (or NOR)

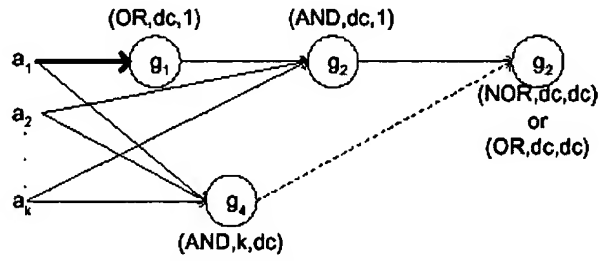


(c) Case 1-3,  $op_1=NOR$ ,  $op_2=NAND$  (or AND); or  $op_1=NAND$ ,  $op_2=OR$ (or NOR)

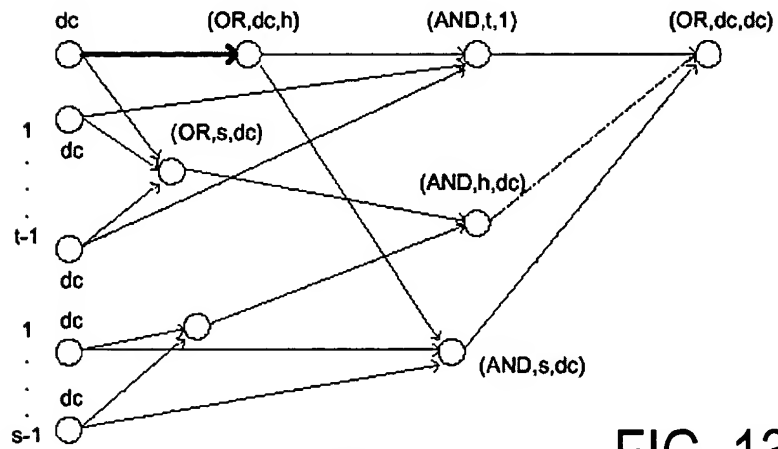
FIG. 12



(a) Case 2-1



(b) Case 2-2



(c) Case 2-3

FIG. 13

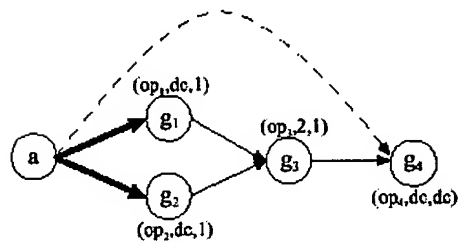
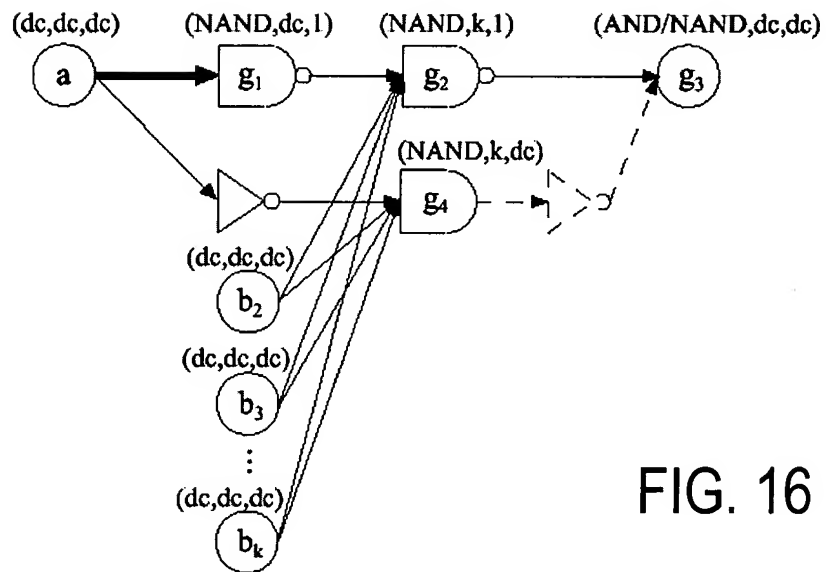
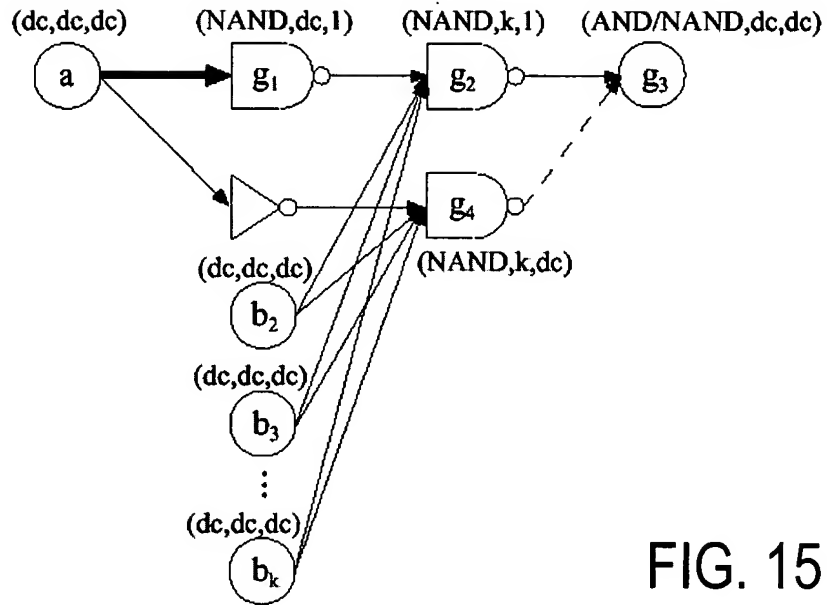


FIG. 14



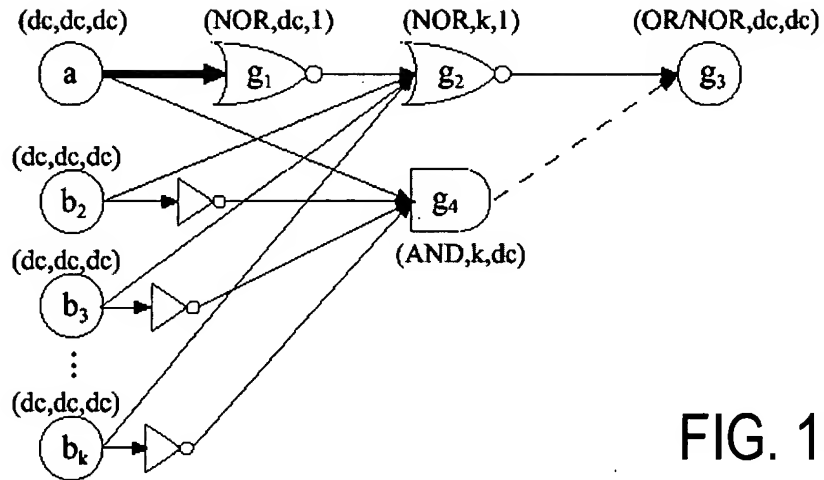


FIG. 17

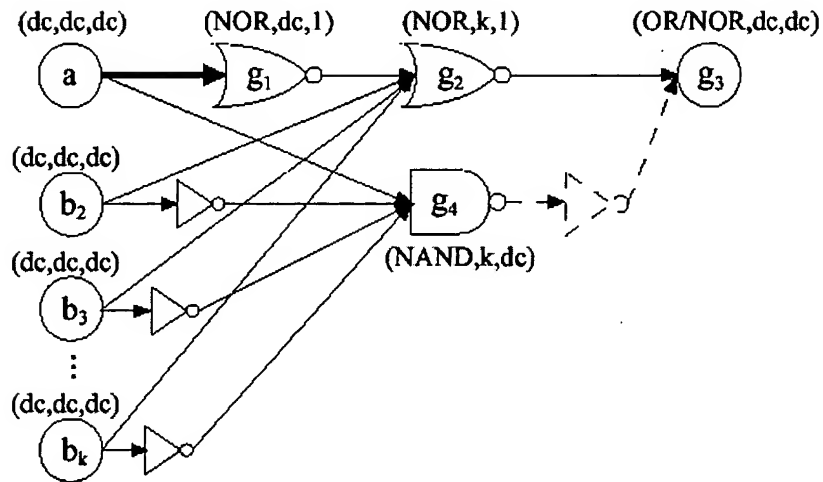


FIG. 18



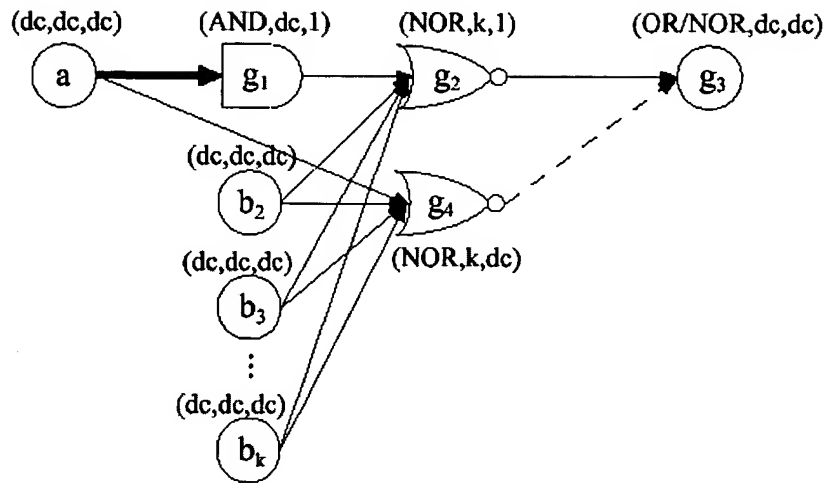


FIG. 19

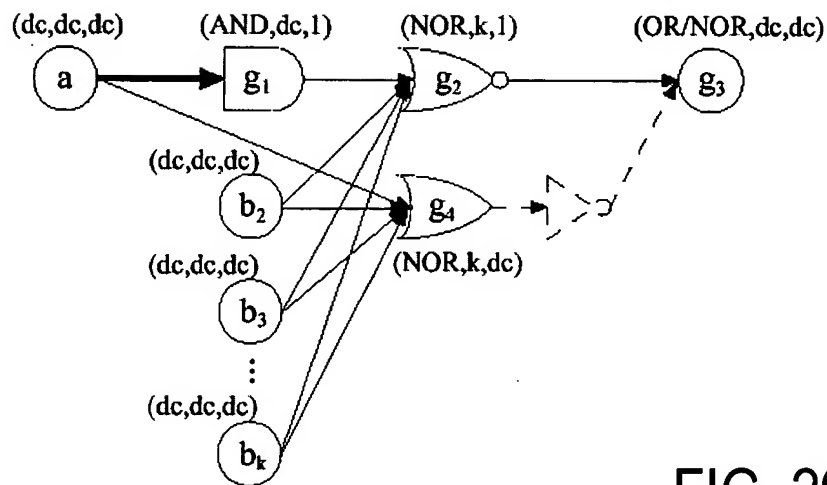


FIG. 20

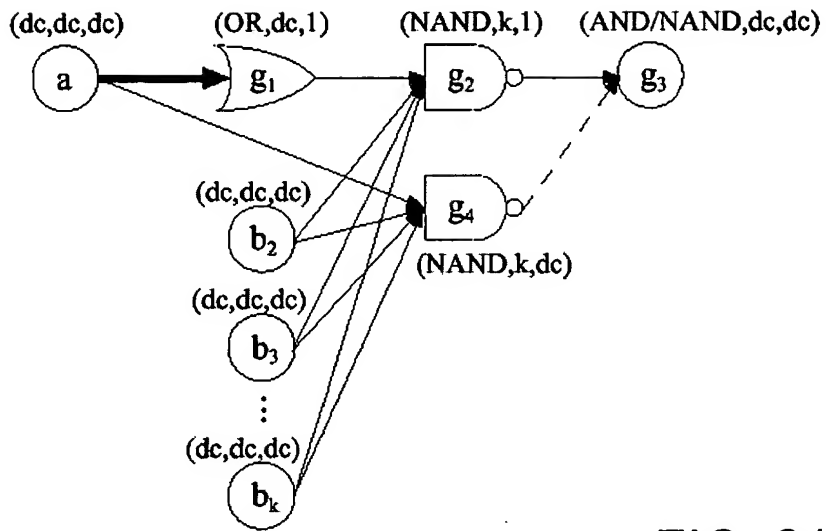


FIG. 21

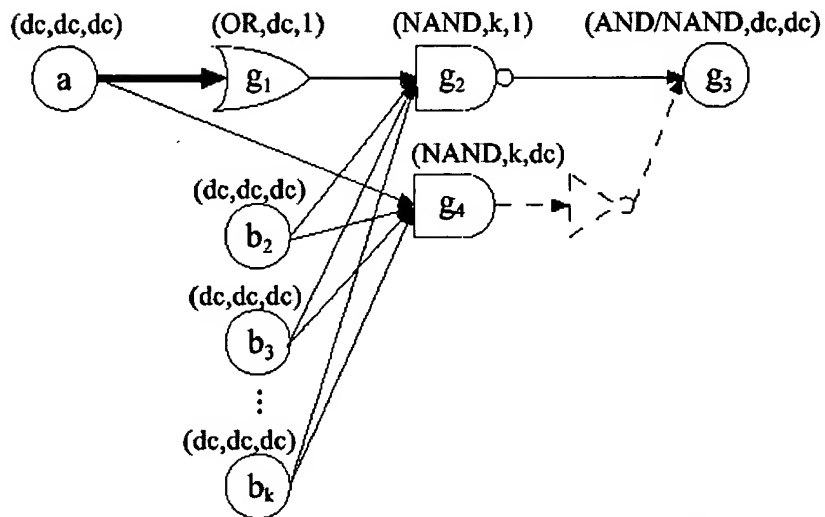


FIG. 22

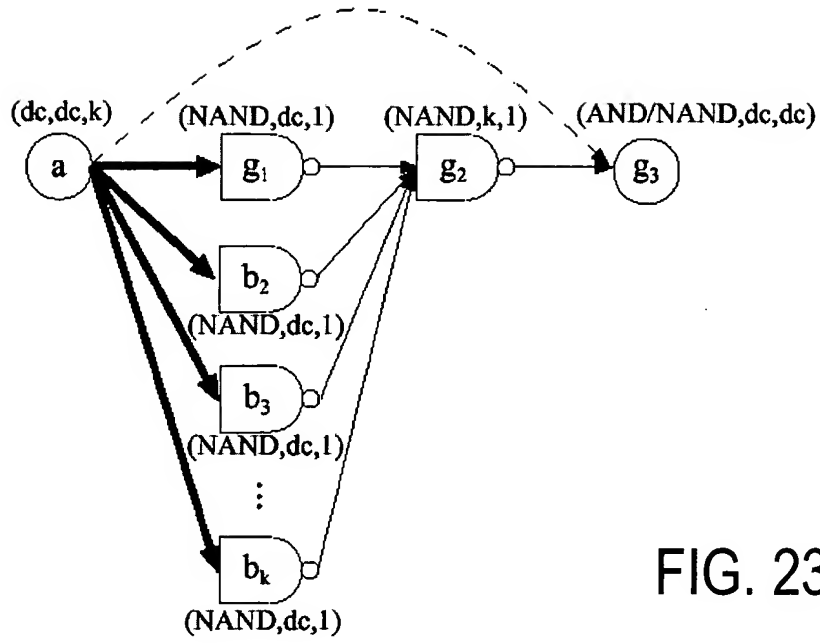


FIG. 23

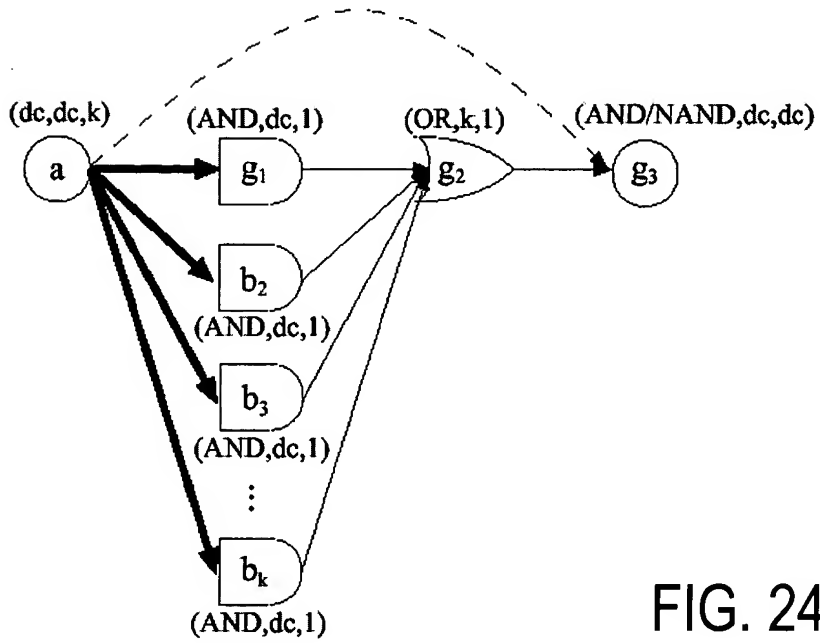


FIG. 24

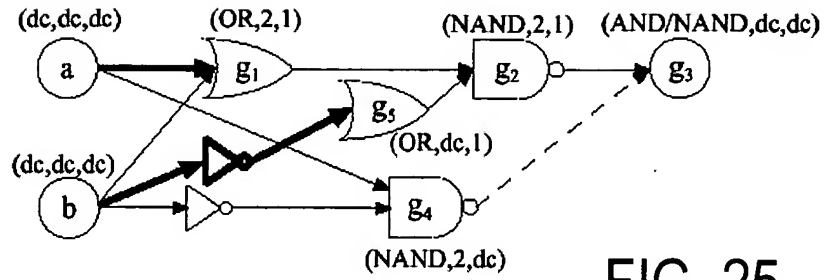


FIG. 25

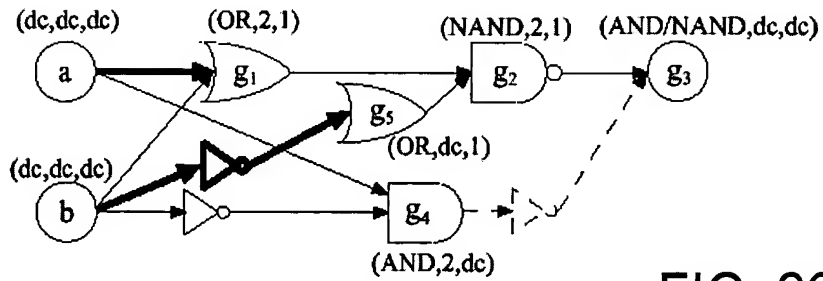


FIG. 26

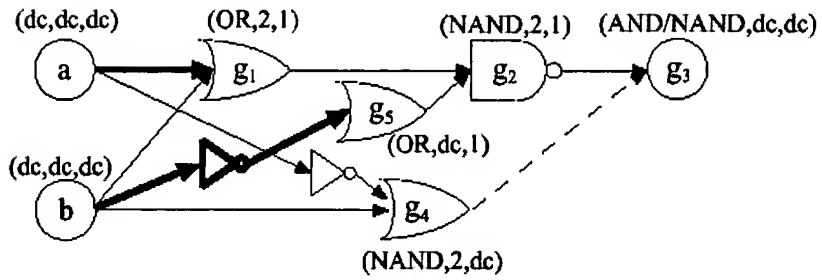


FIG. 27

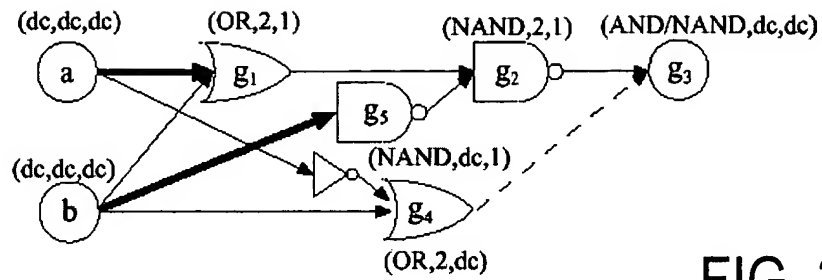


FIG. 28

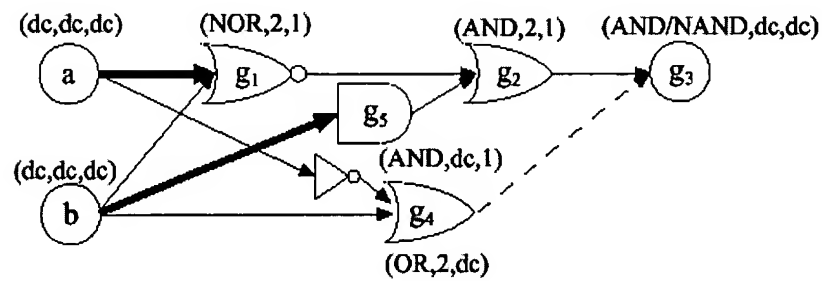


FIG. 29

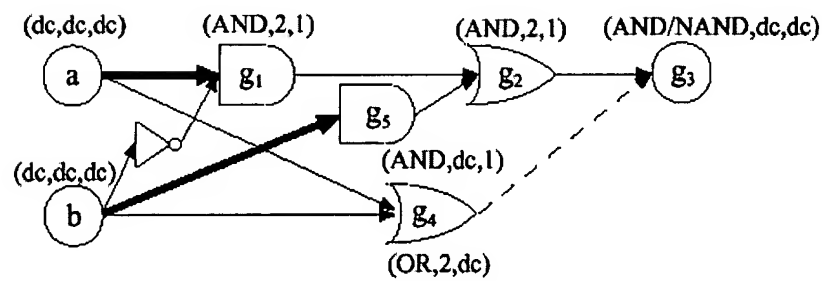


FIG. 30

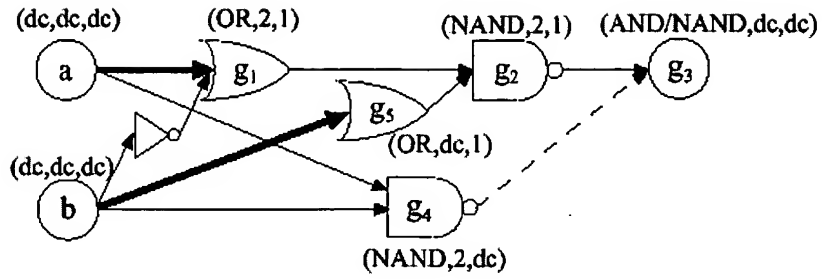


FIG. 31

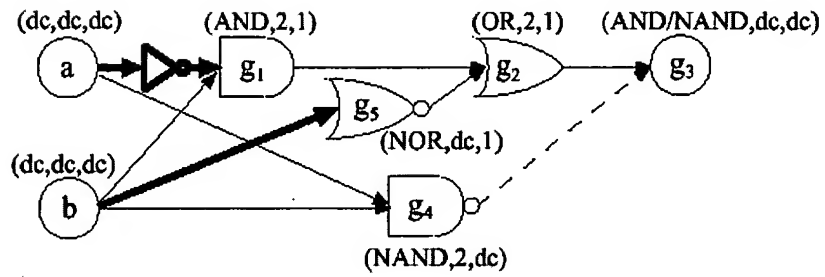


FIG. 32

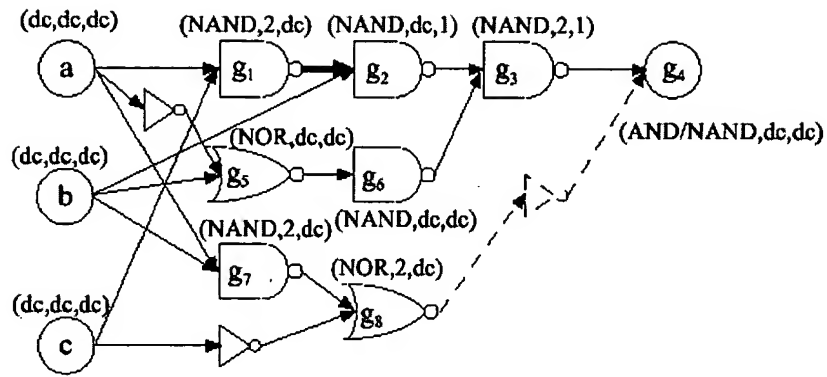


FIG. 33

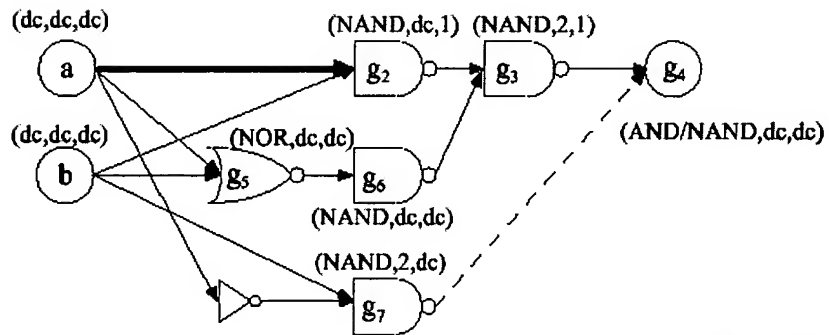


FIG. 34